File   9:Business & Industry(R) Jul/1994-2003/Nov 14
          (c) 2003 Resp. DB Svcs.
File   15:ABI/Inform(R) 1971-2003/Nov 17
          (c) 2003 ProQuest Info&Learning
File   16:Gale Group PROMT(R) 1990-2003/Nov 14
          (c) 2003 The Gale Group
File   20:Dialog Global Reporter 1997-2003/Nov 17
          (c) 2003 The Dialog Corp.
File   47:Gale Group Magazine DB(TM) 1959-2003/Nov 14
          (c) 2003 The Gale group
File   75:TGG Management Contents(R) 86-2003/Nov W2
          (c) 2003 The Gale Group
File   80:TGG Aerospace/Def.Mkts(R) 1986-2003/Nov 14
          (c) 2003 The Gale Group
File   88:Gale Group Business A.R.T.S. 1976-2003/Nov 13
          (c) 2003 The Gale Group
File   98:General Sci Abs/Full-Text 1984-2003/Oct
          (c) 2003 The HW Wilson Co.
File 112:UBM Industry News 1998-2003/Nov 17
          (c) 2003 United Business Media
File 141:Readers Guide 1983-2003/Oct
          (c) 2003 The HW Wilson Co
File 148:Gale Group Trade & Industry DB 1976-2003/Nov 17
          (c)2003 The Gale Group
File 160:Gale Group PROMT(R) 1972-1989
          (c) 1999 The Gale Group
File 275:Gale Group Computer DB(TM) 1983-2003/Nov 14
          (c) 2003 The Gale Group
File 264:DIALOG Defense Newsletters 1989-2003/Nov. 14
          (c) 2003 The Dialog Corp.
File 484:Periodical Abs Plustext 1986-2003/Nov W2
          (c) 2003 ProQuest
File 553:Wilson Bus. Abs. FullText 1982-2003/Oct
          (c) 2003 The HW Wilson Co
File 570:Gale Group MARS(R) 1984-2003/Nov 17
          (c) 2003 The Gale Group
File 608:KR/T Bus.News. 1992-2003/Nov 17
          (c)2003 Knight Ridder/Tribune Bus News
File 620:EIU:Viewswire 2003/Nov 14
          (c) 2003 Economist Intelligence Unit
File 613:PR Newswire 1999-2003/Nov 17
          (c) 2003 PR Newswire Association Inc
File 621:Gale Group New Prod.Annou.(R) 1985-2003/Nov 17
          (c) 2003 The Gale Group
File 623:Business Week 1985-2003/Nov 14
          (c) 2003 The McGraw-Hill Companies Inc
File 624:McGraw-Hill Publications 1985-2003/Nov 14
          (c) 2003 McGraw-Hill Co. Inc
File 634:San Jose Mercury Jun 1985-2003/Nov 15
          (c) 2003 San Jose Mercury News
File 635:Business Dateline(R) 1985-2003/Nov 15
          (c) 2003 ProQuest Info&Learning
File 636:Gale Group Newsletter DB(TM) 1987-2003/Nov 14
          (c) 2003 The Gale Group
File 647:CMP Computer Fulltext 1988-2003/Oct W3
          (c) 2003 CMP Media, LLC
File 696:DIALOG Telecom. Newsletters 1995-2003/Nov 14
          (c) 2003 The Dialog Corp.
File 674:Computer News Fulltext 1989-2003/Nov W2
          (c) 2003 IDG Communications
File 810:Business Wire 1986-1999/Feb 28

File 813:PR Newswire 1987-1999/Apr 30
? ds

Set	Items	Description
S1	19358	(CONVERT? OR CONVERS? OR CHANG? OR MODIF? OR ADJUST? OR ALTER?)(5N)BUS
S2	73	S1(10N)SERIAL?(5N)PARALLEL?
S3	450	S1(5N)(PARTITION? OR DIVID? OR SEPERAT? OR DIVISION? OR PART OR PARTS OR SECTION?? OR SEGMENT?? OR PORTION?? OR FRAGMENT? OR PIECES OR SECTOR??)
S4	1364	ARRAY?(7N)(MEMORY OR BUFFER?? OR STORAGE OR STORAGE(3N)CELLS)(7N)PORT??
S5	1	(ONE OR SINGLE OR 1)()BUFFER?(3N)ELEMENT??
S6	27	CLOCK(5N)(CONTROL OR MONITOR OR DIRECT?)(5N)ACCESS???(7N)(SEQUENTIAL? OR SIMULTANEOUS? OR CONCURRENT? OR COINCIDENT?)
S7	6636	(WRITE OR READ)(3N)CYCLE??
S8	15	AU=(ALOWERSSON, J? OR ROSLUND, B? OR SUNDSTROM, P? OR ALOWERSSON J? OR ROSLUND B? OR SUNDSTROM P?)
S9	10828	(PRESELECT? OR (PRE()(SELECT? OR SET OR DETERMIN? OR SELECT? OR SPECIFIED) OR PREDETERMIN? OR SPECIFIC OR SPECIFIED OR SET OR PRESET))(3N)CYCLE?
S10	0	S1 AND S8
S11	4	S6(S)S7
S12	4	S11 NOT S5
S13	4	RD S12 (unique items)
S14	751	SWITCHCORE
S15	0	S1(S)S14
S16	0	S4(S)S14
S17	731	S14 AND PY=1999:2003
S18	20	S14 NOT S17
S19	10	RD S18 (unique items)
S20	95	S7(S)S9
S21	0	S20(S)S2
S22	0	S20(S)SERIAL?(S)PARALLEL?
S23	0	S3(S)S4
S24	3	S1(S)S4
S25	3	S24 NOT (S13 OR S19)
S26	1	RD S25 (unique items)
S27	4	S4(S)S6
S28	4	S27 NOT (S24 OR S13 OR S19)
S29	1	RD S28 (unique items)
S30	32	S2 AND PY=1999:2003
S31	41	S2 NOT S30
S32	28	RD S31 (unique items)
S33	28	S32 NOT (S27 OR S24 OR S13 OR S19)

02067270   SUPPLIER NUMBER: 06279646
Practical in-place merging. (Research contributions)
Huang, Bing-Chao; Langston, Michael A.
Communications of the ACM, v31, n3, p348(5)
March, 1988
ISSN: 0001-0782   LANGUAGE: English   RECORD TYPE: Fulltext; Abstract
WORD COUNT: 2794   LINE COUNT: 00259

...   first block of the secon sublist. See Figure 4c. We use the
rightmost t.sub. **1** . **buffer   elements** to merge F and G, giving H and I.
See Figure 4d. By virtue of...

...can now move H to its final position by swapping it with the t.sub. **1** .
buffer   elements there. This also restores the buffer. See Figure 4e.
     We now swap the buffer with...
?

13/3,K/1   (Item 1 from file: 16)

04209855   Supplier Number: 46157702   (USE FORMAT 7 FOR FULLTEXT)
Multimedia notebooks going cacheless
Electronic Engineering Times, p47
Feb 19, 1996
Language: English   Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 1120

...   synchronous DRAM (SDRAM), an auto-precharge feature initiates
precharge after the 60-ns initial row **access** , **concurrently** with
pipelined burst- **read**   **cycles** . Unfortunately, SDRAM continues to draw
power due to the continuous 66-MHz **clock** input required to synchronize
its **control** lines. If the SDRAM's clock-disable mode is used to reduce
clock power between...

13/3,K/2   (Item 2 from file: 16)

03568684   Supplier Number: 45015514   (USE FORMAT 7 FOR FULLTEXT)
Motorola Samples Cache Tag RAM, Line Buffers
Electronic News (1991), p78
Sept 26, 1994
Language: English   Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 201

...   starting sometime in 4Q.
    The FIFO-based line buffers feature 4-pin 1149.1 test **access** port
and JTAG bounary-scan support, synchronous and **simultaneous** read/write
memory **access** , 50MHz **clock**   **cycle** times with 15ns **read**   **access**
time, separate read/write counters and up/down counter **control** ,
programmable output/enable **control** and cascadale I/O interfaces.
Manufactured in Motorola's double-metal, doubly -poly 0.65...

13/3,K/3   (Item 1 from file: 148)

08485437   SUPPLIER NUMBER: 18027360   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Multimedia notebooks going cacheless. (Technology Information)
Bondurant, David
Electronic Engineering Times, n889, p47(2)
Feb 19, 1996
ISSN: 0192-1541   LANGUAGE: English   RECORD TYPE: Fulltext; Abstract
WORD COUNT: 1242   LINE COUNT: 00098

...   synchronous DRAM (SDRAM), an auto-precharge feature initiates
precharge after the 60-ns initial row **access** , **concurrently** with
pipelined burst- **read**   **cycles** . Unfortunately, SDRAM continues to draw
power due to the continuous 66-MHz **clock** input required to synchronize
its **control** lines. If the SDRAM's clock-disable mode is used to reduce
clock power between...

13/3,K/4   (Item 1 from file: 647)
DIALOG(R)File 647:CMP Computer Fulltext
(c) 2003 CMP Media, LLC. All rts. reserv.

01082347   CMP ACCESSION NUMBER: EET19960219S0051
Multimedia notebooks going cacheless
David Bondurant, Vice President, Ramtron Enhanced Memory Systems,
    Colorado Springs, Colo
ELECTRONIC ENGINEERING TIMES, 1996, n 889, PG47
PUBLICATION DATE: 960219
JOURNAL CODE: EET   LANGUAGE: English
RECORD TYPE: Fulltext
SECTION HEADING: Special Report - Computer Design
WORD COUNT: 1119

...   synchronous DRAM (SDRAM), an auto-precharge feature initiates
precharge after   the 60-ns initial row **access** , **concurrently** with
pipelined burst- **read**   **cycles** . Unfortunately, SDRAM continues to draw
power due to the continuous 66-MHz **clock** input required to synchronize
its **control** lines. If the SDRAM's clock-disable mode is used to reduce
clock power between...
?

19/3,K/1   (Item 1 from file: 9)
DIALOG(R)File   9:Business & Industry(R)
(c) 2003 Resp. DB Svcs. All rts. reserv.

2299620 Supplier Number: 02299620   (USE FORMAT 7 OR 9 FOR FULLTEXT)
Two edge-router startups target chip, system levels
(SwitchCore **says it is only company to offer true Layer 3 touting in**
  merchant silicon; Redstone Communications solves edge routing problems
  for small carriers and Internet service providers)
Electronic Engineering Times, p 24
November 09, 1998
DOCUMENT TYPE: Journal ISSN: 0192-1541 (United States)
LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 744

      (USE FORMAT 7 OR 9 FOR FULLTEXT)
(SwitchCore **says it is only company to offer true Layer 3 touting in**
  merchant silicon; Redstone...
)

ABSTRACT:
 SwitchCore AB (Sweden), a semiconductor manufacturer just opened US
offices, claims that it is the only...

TEXT:
...chip and system level, prove there is life at the edge where LAN meets
WAN.

 SwitchCore AB, a Swedish semiconductor startup that just opened its U.S.
offices last week, insists...

...more difficult to keep scanning of the header up to speeds required in
modern networks.

 SwitchCore beginnings

Kurt Busch, named last week to be vice president and general manager of the
 SwitchCore U.S. office (San Jose, Calif.), said that his company had
started at the high...

...taped out this month and will be formally introduced in North America in
early 1999.

 SwitchCore 's founders have experience in both advanced CMOS design and
switching topologies. Company founder Christer...
...true single-phase clock latch, which was used in the Alpha processor.
Busch came to **SwitchCore** after learning about the company during his
years at Digital Equipment Corp.'s semiconductor group...

...logic were semicustom, but the data-path portion of the chip was
full-custom layout.

 SwitchCore will not target LAN workgroups but will instead aim its chip
sets directly at enterprise...

...COMPANY NAMES: **SWITCHCORE** AB

 19/3,K/2   (Item 1 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)

05939889   Supplier Number: 53190668   (USE FORMAT 7 FOR FULLTEXT.)
Two edge-router startups target chip, system levels.(SwitchCore **AB**
  CXe-16, Redstone Communications RX Edge Switch Router) (Product
  Announcement)
Wirbel, Loring
Electronic Engineering Times, p24(1)
Nov 9, 1998
Language: English   Record Type: Fulltext
Article Type: Product Announcement
Document Type: Magazine/Journal; Trade
Word Count: 748

Two edge-router startups target chip, system levels.(SwitchCore **AB**
  CXe-16, Redstone Communications RX Edge Switch Router) (Product
  Announcement)
      SwitchCore AB, a Swedish semiconductor startup that just opened its
U.S. offices last week, insists...

...more difficult to keep scanning of the header up to speeds required in
modern networks.
      SwitchCore beginnings
      Kurt Busch, named last week to be vice president and general manager
of the **SwitchCore** U.S. office (San Jose, Calif.), said that his company
had started at the high...

...taped out this month and will be formally introduced in North America in
early 1999.
      SwitchCore 's founders have experience in both advanced CMOS design
and switching topologies. Company founder Christer...

...true single-phase clock latch, which was used in the Alpha processor.
Busch came to **SwitchCore** after learning about the company during his
years at Digital Equipment Corp.'s semiconductor group...

...logic were semicustom, but the data-path portion of the chip was
full-custom layout.
      SwitchCore will not target LAN workgroups but will instead aim its
chip sets directly at enterprise...
COMPANY NAMES: Redstone Communications Inc.; **SwitchCore** AB
TRADE NAMES: Redstone Communications RX Internet Edge Router
  (Bridge/router); **SwitchCore** CXe-16 (Bridge/router)

 19/3,K/3   (Item 2 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)

05919996   Supplier Number: 53152899   (USE FORMAT 7 FOR FULLTEXT)
 SwitchCore **Opens US Sales and Marketing Office; Gigabit Ethernet Switch**
  Maker Set to Meet the Needs of Networking Industry.
Business Wire, p1009
Nov 2, 1998
Language: English   Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 371

  (USE FORMAT 7 FOR FULLTEXT)
 SwitchCore **Opens US Sales and Marketing Office; Gigabit Ethernet Switch**

Maker Set to Meet the Needs...
TEXT:
SAN JOSE, Calif.--(BUSINESS WIRE)--Nov. 2, 1998-- **SwitchCore** AB, a
developer of single-chip switches and wire-speed routers, today announced
the opening of a sales and marketing office in San Jose, California.
SwitchCore 's corporate headquarters is located in Lund, Sweden, with a
design center in Stockholm, Sweden.
      SwitchCore was founded in 1997 by professor Christer Svensson Ph.D.,
Per Andersson Ph.D. and...

...Institute of Technology. Additional assistance for the project came from
Ericsson and SAAB Dynamics.
      The **SwitchCore** architecture allows wire-speed switching and routing
to be integrated into single-chip solutions. **SwitchCore** 's chip
architecture uses a full-custom design methodology coupled with a
leading-edge process...

...previously used only for memories and CPUs and not typically used in
datacom.
      Kurt Busch, **SwitchCore** 's vice president and general manager, will
manage the new office. The three-person California...

...and design at SMC and Digital Equipment Corporation.
      "Kurt fills a key strategic role at **SwitchCore** ," said Per
Andersson, president of **SwitchCore** . "He brings a proven track record of
placing new silicon solutions in the marketplace, and...

...networking industry. We are extremely pleased to have Kurt on board, not
only to develop **SwitchCore** 's business, but also to evolve our identity as
we continue to grow."
      According to...

...companies targeting the hot Ethernet switch IC market, a global presence
has become a necessity. **SwitchCore** 's silicon valley office will help the
company meet the demands of its future customers."
      About **SwitchCore** Corporation
      SwitchCore is a fabless semiconductor company that uses a
full-custom design methodology to achieve the highest levels of integration
and performance. **SwitchCore** was founded in 1997 and has offices in San
Jose, California, Lund and Stockholm, Sweden. Additional information about
SwitchCore is available on the Internet at http://www. **switchcore** .com
      For more information about **SwitchCore** please contact:
      SwitchCore Corporation 675 N. First Street, Suite PH3 San Jose, CA
95112 Tel: (408) 995-3850 Fax: (408) 995-3858
      SwitchCore is a trademark of **SwitchCore** Corporation.

 19/3,K/4   (Item 1 from file: 160)
DIALOG(R)File 160:Gale Group PROMT(R)
(c) 1999 The Gale Group. All rts. reserv.

02362676
Brady (WH) - Research & Development Outlays
Annual Report   1989   p. 0

      ... Corporate materials scientists serve as resources in polymer
chemistry, physics and materials.
      The capacitive membrane **switchcore** product line, introduced in 1985
and now marketed by the Xymox Division, is an example...

19/3,K/5   (Item 2 from file: 160)
DIALOG(R)File 160:Gale Group PROMT(R)
(c) 1999 The Gale Group. All rts. reserv.

01650787
THE W H BRADY CO XYMOX (R) DIVISION INTRODUCES CAPACITIVE SWITCHCORE
     EVALUATION KIT.
NEWS RELEASE   May 11, 1987   p. 11

     ... assist potential users in evaluating this advanced technology
development, Xymox has packaged a special Capacitive **Switchcore**
Evaluation Kit. Contained in the kit are IBM-PC style open and sealed
capacitive switchcores...

19/3,K/6   (Item 3 from file: 160)
DIALOG(R)File 160:Gale Group PROMT(R)
(c) 1999 The Gale Group. All rts. reserv.

01623480
W H BRADY CO 'S XYMOX (R) DIVISION INTRODUCES LINE OF CAPACITIVE KEYBOARD
     SWITCHCORES.
NEWS RELEASE   April 9, 1987   p. 11

     ... been awarded several United States patents covering the unique
process for achieving capacitance in the **switchcore** .

19/3,K/7   (Item 1 from file: 621)
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)
(c) 2003 The Gale Group. All rts. reserv.

01023979   Supplier Number: 39718960   (USE FORMAT 7 FOR FULLTEXT)
W.H. BRADY CO.'S XYMOX (R) DIVISION, INTRODUCES NEW LINE OF CAPACITIVE AND
   CONTACT KEYBOARD SWITCHCORES
PR Newswire, pN/A
March 21, 1986
Language: English   Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 316

...   562,315 and 4,367,385, covering the unique process for
     achieving capacitance in the **switchcore** .

     Xymox has packaged a special Capacitive **Switchcore** Evaluation Kit,
     which contains: sample open and sealed capacitive switchcores,
     necessary interface electronics, and an...

19/3,K/8   (Item 1 from file: 635)
DIALOG(R)File 635:Business Dateline(R)
(c) 2003 ProQuest Info&Learning. All rts. reserv.

0187304 91-08502
W.H. Brady Won't Buy Johnson Controls Building, Needs More Space
Kirchen, Rich

The Business Journal-Milwaukee (Milwaukee, WI, US), V8 N13 s1 p3
PUBL DATE: 910107
WORD COUNT: 301
DATELINE: Milwaukee, WI, US

TEXT:

     ...the Milwaukee area are coated products, industrial products,
specialty tape products, Xymox switch and keyboard **switchcore** products,
and Signmark facility and safety identification products.

     The company has not set a timetable...

 19/3,K/9   (Item 1 from file: 647)
DIALOG(R)File 647:CMP Computer Fulltext
(c) 2003 CMP Media, LLC. All rts. reserv.

01177746   CMP ACCESSION NUMBER: EET19981109S0029
Two edge-router startups target chip, system levels
Loring Wirbel
ELECTRONIC ENGINEERING TIMES, 1998, n 1034, PG24
PUBLICATION DATE: 981109
JOURNAL CODE: EET   LANGUAGE: English
RECORD TYPE: Fulltext
SECTION HEADING: News
WORD COUNT: 754

     SwitchCore AB, a Swedish semiconductor startup that just opened its
U.S. offices last week, insists...

...more difficult to keep scanning of the header up to speeds required in
modern networks.

  SwitchCore beginnings

 Kurt Busch, named last week to be vice president and general
manager of the **SwitchCore** U.S. office (San Jose, Calif.), said that his
company had started at the high...

...taped out this month and will be formally introduced in North America
in early 1999.

  SwitchCore 's founders have experience in both advanced CMOS design
and switching topologies. Company founder Christer...

...true single-phase clock latch, which was used in the Alpha processor.
Busch came to **SwitchCore** after learning about the company during his
years at Digital Equipment Corp.'s semiconductor group...

...logic were semicustom, but the data-path portion of the chip was
full-custom layout.

  SwitchCore will not target LAN workgroups but will instead aim its
chip sets directly at enterprise...
...COMPANY NAMES (DIALOG GENERATED): Cisco Systems Inc ; Digital Equipment
  Corp ; MMC Networks Inc ; Next Generation Networks ; Redstone
  Communications Inc ; **SwitchCore** AB ; **SwitchCore** U S

19/3,K/10   (Item 1 from file: 810)
DIALOG(R)File 810:Business Wire

0932723   BW0485

Business Wire Recap

November 02, 1998

Byline: EDITORS

    ...LINK) LITTLETON, Colo.--Oilfield Equipment Manager and
Oil-Link Establish Affiliate Partnership (BW0017 05:58)
    (**SWITCHCORE**) SAN JOSE, Calif.-- **SwitchCore** Opens US Sales and
Marketing Office; Gigabit Ethernet Switch Maker Set to Meet the Needs...
?

26/3,K/1   (Item 1 from file: 16)
DIALOG(R)File   16:Gale Group PROMT(R)
(c) 2003 The Gale Group. All rts. reserv.

09171157   Supplier Number: 80004422   (USE FORMAT 7 FOR FULLTEXT)
**QLogic Simplifies Storage Networks for Small and Medium Sized Businesses
  with SAN Connectivity Kit, Priced at $9,999.**
Business Wire, p2095
Nov 13, 2001
Language: English   Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 1116

...   carrying case

    -- Hard copy documentation

    -- One year warranty

    -- Computer Based Training CD
       "With eight switch **ports** and four host bus adapters, the SAN
Connectivity Kit can connect four servers, two disk **storage   arrays** and
a tape library -- a typical four-node Exchange or SQL workgroup," said
Frank Berry, vice president of corporate marketing for QLogic. "
Alternatively , by installing dual host **bus** adapters in each of two
servers with the HBA failover software, the SAN Connectivity Kit...
?

08256051   Supplier Number: 69654216 (USE FORMAT 7 FOR FULLTEXT)
**IDT Introduces Industry's Fastest and Highest-Density Synchronous
  Dual-Port.**
Business Wire, p0205
Jan 29, 2001
Language: English   Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 954

...   traditional dual-port architecture, without sacrificing operating
speed or flexibility.
    The device has two independent **ports** with separate **control** ,
address, **clock** and I/O pins, allowing both **ports   simultaneous   access**
 to the memory **array** . Each **port** can **access** any **memory** block not in
use by the opposite **port** . Bank address pins **control** accesses by the
ports into specific banks through asserting standard addresses. This
creates flexibility in...
?

33/3,K/1   (Item 1 from file: 9)
DIALOG(R)File   9:Business & Industry(R)
(c) 2003 Resp. DB Svcs. All rts. reserv.

1954876   Supplier Number: 01954876   (USE FORMAT 7 OR 9 FOR FULLTEXT)
Pentium board can fit in a wallet
(Cell Computing introduces a credit-card-size Pentium motherboard that
** measures 3.4 in x 2.1 in x 0.8 in)**
Electronic Buyers News, p 38
October 06, 1997
DOCUMENT TYPE: Journal ISSN: 0164-6362   (United States)
LANGUAGE: English RECORD TYPE: Fulltext
WORD COUNT: 370

     (USE FORMAT 7 OR 9 FOR FULLTEXT)

TEXT:
...a wide range of additional functions to be attached to the card,
including Ethernet, modem, **serial** , and **parallel** ports.

To further reduce design time, the ISA **bus** can be **converted** into a
PCMCIA connection that uses off-the-shelf cards. A small-outline DIMM
socket...

 33/3,K/2   (Item 1 from file: 15)
DIALOG(R)File 15:ABI/Inform(R)
(c) 2003 ProQuest Info&Learning. All rts. reserv.

01113174 97-62568
Gemini Axiomatic RS-232/PC Card Converter
Fornagiel, John; Zervos, Ilias
Cellular Business v12n10 PP: 92-96 Oct 1995
ISSN: 0741-6520 JRNL CODE: CLB
WORD COUNT: 1569

...ABSTRACT: device turns a standard communications port into a PCMCIA
port. The Axiomatic PCMCIA Interface transparently **converts** PCMCIA
 parallel   **bus**   signals from PCMCIA I/O cards into standard RS-232 **serial**
 signals. It allows non-PCMCIA devices to run PCMCIA modems via the serial
port. Overall...
...TEXT: Now you can overcome these limitations with the Axiomatic
Interface.

The Axiomatic PCMCIA Interface transparently **converts** PCMCIA **parallel**
 bus   signals from PCMCIA I/O cards into standard RS-232 **serial** signals.
The Axiomatic allows non-PCMCIA devices to run PCMCIA modems via the serial
port...

 33/3,K/3   (Item 1 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)
(c) 2003 The Gale Group. All rts. reserv.

05274476   Supplier Number: 48034901 (USE FORMAT 7 FOR FULLTEXT)
Chips: Cell Announces Industry's First Credit-Card-Sized Pentium
** Motherboard; Reduces Costs, Simplifies Design and Shortens Time to Market**
EDGE: Work-Group Computing Report, pN/A
Oct 6, 1997
Language: English   Record Type: Fulltext

Document Type: Newsletter; Trade
Word Count: 743

...   a wide range of additional functions to be attached to the CardPC
including Ethernet, modem, **serial** , **parallel** ports.
     To further reduce design time, the ISA **bus** can be .**converted** into a
PCMCIA connection that uses off-the-shelf cards. A Small Outline DIMM
socket...

 33/3,K/4   (Item 2 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)

03678630   Supplier Number: 45200520   (USE FORMAT 7 FOR FULLTEXT)
Bus spec charged up
Electronic Engineering Times, p16
Dec 12, 1994
Language: English   Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 593

...   Access.bus protocol that ABIG's subcommittee has drafted. That
version is designed for the **parallel** port, but Momentum said it will
convert it to the **serial** Access. **bus** port when the market is ready. The
product is being targeted at wireless networking, according...

 33/3,K/5   (Item 3 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)

03273734   Supplier Number: 44510943   (USE FORMAT 7 FOR FULLTEXT)
**HIGH-SPEED COMMUNICATION CMOS CELLS FROM VLSI SIMPLIFY SONET/SDH AND ATM
  SYSTEM DESIGN**
News Release, pN/A
March 14, 1994
Language: English   Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 646

...   silicon
     chips. The first, a pair of 155 MHz input/output cells, permits
     high'speed **serial**
 data transmission. This eliminates the need for and
     design time associated with circuitry required to **convert** to a
      parallel signal **bus** for interfacing with other chips or with the
     system backplane. Cost is also reduced over...

 33/3,K/6   (Item 4 from file: 16)
DIALOG(R)File 16:Gale Group PROMT(R)

03175782   Supplier Number: 44342659   (USE FORMAT 7 FOR FULLTEXT)
**CONTROL INTERFACES ARE KEY COMPONENT OF HONEYWELL'S NEW SDS SMART
  DISTRIBUTED SYSTEM**
News Release, pN/A
Jan 10, 1994
Language: English   Record Type: Fulltext

Document Type: Magazine/Journal; Trade
Word Count: 372

...   the field devices and a PLC when a direct interface is not
      available. The ITS **converts** data on the four-wire **bus** from **serial**
      data into **parallel** HO points. GE Fanuc is developing an interface to
      the series 90-30 PLC. FACTS...

 33/3,K/7   (Item 1 from file: 47)

02435313   SUPPLIER NUMBER: 02827406   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Data storage in a nutshell.
Solomon, Les; Veit, Stan
Computers & Electronics, v21, p36(9)
July, 1983
ISSN: 0745-1458   LANGUAGE: ENGLISH   RECORD TYPE: FULLTEXT
WORD COUNT: 6986   LINE COUNT: 00522

...   the disk storage system with the CPU and memory. Data is written
and read in **serial** format, but the controller converts it to put it on
the CPU data **bus** . Once the **serial** -to- **parallel** **conversion** has been
performed, the controller generates an interrupt of the CPU so that it can
...

 33/3,K/8   (Item 1 from file: 148)

10331882   SUPPLIER NUMBER: 20929112   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Complete GPS Receiver Fits On Two Chips.
Vollmer, Alfred
Electronic Design, v46, n16, p50(1)
July 6, 1998
ISSN: 0013-4872   LANGUAGE: English   RECORD TYPE: Fulltext; Abstract
WORD COUNT: 3069   LINE COUNT: 00235

...   diagnostic controller. A GPS used in an automotive application may
require an analog-to-digital **converter** (ADC) and a vehicle **bus**
interface. In this case, an external ADO is used, and **serially** interfaced
via the **parallel** I/O pins.
      The watchdog offers a single timeout period of2s, and the software is
...

 33/3,K/9   (Item 2 from file: 148)

09751822   SUPPLIER NUMBER: 19792540   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Cell Announces Industry's First Credit-Card-Sized Pentium Motherboard;
  Reduces Costs, Simplifies Design and Shortens Time to Market.
Business Wire, p9291131
Sep 29, 1997
LANGUAGE: English   RECORD TYPE: Fulltext
WORD COUNT: 849   LINE COUNT: 00072

... a wide range of additional functions to be attached to the CardPC including Ethernet, modem, **serial** , **parallel** ports.

To further reduce design time, the ISA **bus** can be **converted** into a PCMCIA connection that uses off-the-shelf cards. A Small Outline DIMM socket...

33/3,K/10   (Item 3 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
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05230233   SUPPLIER NUMBER: 10627117   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Electro goes international. (The East Coast regional conference and exhibition changes name from Electro to Electro/International)
Novellino, John
Electronic Design, v39, n7, p41(4)
April 11, 1991
ISSN: 0013-4872   LANGUAGE: ENGLISH   RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 1066   LINE COUNT: 00089

... describes a parallel/serial converter. The device acts as an interface between a circuit's **parallel** host bus and the **serial** test **bus** .

The **conversion** is managed by a bank of double-buffered, **parallel** -to- **serial** shift registers. Brown notes that double buffering allows a **parallel** write or read to occur while data is shifting, increasing throughput. The serial port's...

33/3,K/11   (Item 4 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2003 The Gale Group. All rts. reserv.

03289534   SUPPLIER NUMBER: 05113345   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Control MIL-STD-1553 with remote terminal chips.
Snyder, Dennis R.
Electronic Design, v35, p109(5)
Aug 6, 1987
ISSN: 0013-4872   LANGUAGE: ENGLISH   RECORD TYPE: FULLTEXT
WORD COUNT: 2671   LINE COUNT: 00218

... RAO/RAZ inputs for Channel A and RBZ/RBO are inputs for Channel B).

A **serial - parallel** **converter** connects the internal 16-bit **bus** to the Manchester encoder/decoder. To limit transmission time, according to MIL-STD-1553B, to...

33/3,K/12   (Item 5 from file: 148)
DIALOG(R)File 148:Gale Group Trade & Industry DB
(c)2003 The Gale Group. All rts. reserv.

03118904   SUPPLIER NUMBER: 04683056   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Serial data races at parallel rates for the best of both worlds.
Connor, Gary; Scott, Paul; Vesuna, Sarosh
Electronic Design, v35, p79(5)
Jan 22, 1987
ISSN: 0013-4872   LANGUAGE: ENGLISH   RECORD TYPE: FULLTEXT
WORD COUNT: 2561   LINE COUNT: 00199

... at high data rates.

TRADING COPPER FOR SILICON
    Most new designs avoid multiwire links by converting the parallel
bus to a serial bit stream. But with current technology, these
interfaces trade excessive copper for excessive silicon. A...

 33/3,K/13   (Item 1 from file: 160)
DIALOG(R)File 160:Gale Group PROMT(R)
(c) 1999 The Gale Group. All rts. reserv.

01900853
On-chip testing dramatically drops cost of diagnostics
Electronics   March 17, 1988   p. 34
ISSN: 0883-4989

    ... onto 6 chips by the end of 1988. The feature is made up of
registers,   **serial - parallel   converting** circuits, and a transfer **bus**
that provides the block access to various functional blocks on an IC. Four
pins/chip...

 33/3,K/14   (Item 2 from file: 160)
DIALOG(R)File 160:Gale Group PROMT(R)
(c) 1999 The Gale Group. All rts. reserv.

01025029
New product previews: High-speed graphics chip set reduces burden on host
    microprocessor.
Electronics   May 3, 1984   p. 84,186

    ... will use a CMOS gate array to support the GMP. VSR chip functions
include data **bus** control, frame buffer, and the **conversion** of **parallel**
 pixel data from the frame buffer into the **serial** form needed to drive a
color-pallet random access memory. To be introduced in 3rd...

 33/3,K/15   (Item 1 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2003 The Gale Group. All rts. reserv.

02167099   SUPPLIER NUMBER: 20168240   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Ride the new wave in scanning. (EasyPhoto ImageWave) (Software
   Review)(Brief Article)(Evaluation)
Leemon, Sheldon
Computer Shopper, v18, n2, p442(1)
Feb, 1998
DOCUMENT TYPE: Brief Article Evaluation   ISSN: 0886-0556
LANGUAGE: English   RECORD TYPE: Fulltext
WORD COUNT: 971   LINE COUNT: 00080

...   with devices such as tape backup drives and Zip drives.
    In addition to the standard **parallel** -port interface, Storm offers an
optional $29.95-estimated Universal **Serial   Bus   converter** . Since the
USB interface is likely to become popular after the release of Windows 98
...

 33/3,K/16   (Item 2 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2003 The Gale Group. All rts. reserv.

02074138   SUPPLIER NUMBER: 19516661   (USE FORMAT 7 OR 9 FOR FULL TEXT)
OmniPost Analyzer Kit helps tackle your PC problems. (Omni Diagnostics
  hardware/software analysis system) (Hardware Review)(Evaluation)
O'Brien, Bill
Computer Shopper, v17, n7, p531(1)
July, 1997
DOCUMENT TYPE: Evaluation   ISSN: 0886-0556   LANGUAGE: English
RECORD TYPE: Fulltext; Abstract
WORD COUNT: 454   LINE COUNT: 00038

...ABSTRACT: consists of a plug-in analyzer card, a bus extender card for
ISA or VL- **Bus** , port **converters** that **change parallel** and **serial**
ports between 9 and 25 pins and DOS-based application software called PC
Check. Users...

 33/3,K/17   (Item 3 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2003 The Gale Group. All rts. reserv.

01372015   SUPPLIER NUMBER: 08777254   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Calling long-distance: three useful products - AdComm99, COMET, and
  SilverComm - let you add communications to your dBASE language
  applications. (Software Review) (evaluation)
Tamburrino, Jim
Data Based Advisor, v8, n8, p100(6)
August, 1990
DOCUMENT TYPE: evaluation   ISSN: 0740-5200   LANGUAGE: ENGLISH
RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 2803   LINE COUNT: 00222

...   Before I dig into each product, you may be wondering why they're
necessary. PC **serial** -communications functions are processed by the
Universal Asynchronous Receiver/Transmitter, better known as UART. This
device **converts** the **bus** ' eight **parallel** data paths into the
sequential, single-path, 10-bit structure used in asynchronous **serial**
I/O. The UART converts one character at a time and sends or receives the...

 33/3,K/18   (Item 4 from file: 275)
DIALOG(R)File 275:Gale Group Computer DB(TM)
(c) 2003 The Gale Group. All rts. reserv.

01310501   SUPPLIER NUMBER: 07651454   (USE FORMAT 7 OR 9 FOR FULL TEXT)
MIL-STD-1553 takes over serial data bus designs. (includes related article
  on a crucial protocol device in 1553 interface design)
Ziegler, Jeff
Computer Design, v28, n17, p99(4)
Sept 1, 1989
ISSN: 0010-4566   LANGUAGE: ENGLISH   RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 2969   LINE COUNT: 00231

    With the recent proliferation of families of monolithic 1553 interface
circuits, a 1553 data **bus0** is becoming an economical **alternative** to
other **serial** and **parallel** -type data buses. Originally developed to
simplify the system integration problems encountered in airborne systems...

 33/3,K/19   (Item 5 from file: 275)

01304034   SUPPLIER NUMBER: 07312998   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Motorola rethinks the fundamentals for one-chip 32-bit microcontroller.
Wilson, Ron
Computer Design, v28, n11, p20(2)
June 1, 1989
ISSN: 0010-4566   LANGUAGE: ENGLISH   RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 1266   LINE COUNT: 00098

...   QSPI. The bus control tasks are handled by a local processor, which
not only manages **serial / parallel conversion** and **bus** control
signals, but also maintains a set of data queues in a small local RAM...

 33/3,K/20   **(Item 6 from file: 275)**

01258008   SUPPLIER NUMBER: 06808438   (USE FORMAT 7 OR 9 FOR FULL TEXT)
Single-chip modems: hurt and help sales.
Chartock, David S.
Computer & Software News, v6, n45, p79(1)
Nov 7, 1988
ISSN: 0745-5291   LANGUAGE: ENGLISH   RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 536   LINE COUNT: 00040

...   would also require an 8250 UART (Universal Asynchronous Reversal
Transmitter) interfaces for a microcomputer's **parallel bus** so signals
can be **converted** into a **serial** format.
     Bell 212A single-chip modems from AMD include the AM7901. Other
single-chip modems...

 33/3,K/21   **(Item 7 from file: 275)**

01244877   SUPPLIER NUMBER: 06294654   (USE FORMAT 7 OR 9 FOR FULL TEXT)
**STE bus: ready to break into U.S. market? (System Technology: computers and
   computer subsystems.)**
Mayer, John
Computer Design, v27, n6, p27(2)
March 15, 1988
ISSN: 0010-4566   LANGUAGE: ENGLISH   RECORD TYPE: FULLTEXT; ABSTRACT
WORD COUNT: 1012   LINE COUNT: 00078

...   around an 80C88-based single-board computer, the products will
include boards for signal processing, **parallel** and **serial**0 I/O, memory
and analog-to-digitial **conversion** among others.
     For STE **Bus** to have a significant impact on the low-end arena,
however, many observers foresee the...

 33/3,K/22   **(Item 1 from file: 621)**

01088388   Supplier Number: 40544373   (USE FORMAT 7 FOR FULLTEXT)

CTI/dbx 18/19/20-BIT ANALOG-TO-DIGITAL CONVERTER CHIP SET AND EVALUATION
  BOARD NOW AVAILABLE
News Release, p1
Oct 18, 1988
Language: English   Record Type: Fulltext
Document Type: Magazine/Journal; Trade
Word Count: 726

...   and therefore the entire decimator, is linear
    phase.

    The final output is available in both **serial** and **parallel** formats.
    Synchronizing clock outputs are provided for both.

    A common **bus** configuration for multiple **converters** as well as
    straightforward processor interfacing is made possible by tristate
    drivers for the parallel...

 33/3,K/23   (Item 2 from file: 621)
DIALOG(R)File 621:Gale Group New Prod.Annou.(R)
(c) 2003 The Gale Group. All rts. reserv.

01010751   Supplier Number: 39596456   (USE FORMAT 7 FOR FULLTEXT)
SIGNETICS EXPANDS PROGRAMMABLE LOGIC SEQUENCER LINE
PR Newswire, pN/A
Sept 25, 1985
Language: English   Record Type: Fulltext
Document Type: Newswire; Trade
Word Count: 495

...   applications for the PLS168/168A include interface protocol
    variable module counters, sequential and peripheral controllers,
     parallel - **serial**   **converters** and **bus** arbitrators.
    All Signetics Programmable Logic Devices are supported by AMAZE
    (Automated MAP and ZAP Equation...

 33/3,K/24   (Item 1 from file: 636)
DIALOG(R)File 636:Gale Group Newsletter DB(TM)
(c) 2003 The Gale Group. All rts. reserv.

03752227   Supplier Number: 48116521   (USE FORMAT 7 FOR FULLTEXT)
Creative May Be Eyeing Home Theater Market with Cambridge Acquisition
Multimedia Week, v6, n44, pN/A
Nov 10, 1997
Language: English   Record Type: Fulltext
Document Type: Newsletter; Trade
Word Count: 924

...   card. While digital audio device manufacturers mull whether to base
their products on the Universal **Serial** Bus (USB), some analysts believe
the company may be hedging its bets.
    The Universal **Serial** **Bus** (USB) offers an **alternative** to
attaching peripheral devices to computers other than traditional **serial** ,
SCSI and **parallel** ports. Most OEMs either offer or plan to offer the
Intel- backed USB on their...

 33/3,K/25   (Item 1 from file: 647)

01140367   CMP ACCESSION NUMBER: EBN19971006S0076
Pentium board can fit in a wallet
ELECTRONIC BUYER'S NEWS, 1997, n 1078, PG38
PUBLICATION DATE: 971006
JOURNAL CODE: EBN   LANGUAGE: English
RECORD TYPE: Fulltext
SECTION HEADING: Products - Digital Semiconductors
WORD COUNT: 380

...   a wide range of additional functions to be attached to the card,
including Ethernet, modem, **serial** , and **parallel**   ports.
      To further reduce design time, the ISA **bus** can be **converted** into
a PCMCIA connection that uses off-the-shelf cards. A small-outline DIMM
socket...

 33/3,K/26   (Item 2 from file: 647)

01038014   CMP ACCESSION NUMBER: EET19941212S0023
Bus spec charged up-EXPANDED ACCESS.BUS WILL ACCOMMODATE   INTEL'S SMBUS (
late news)
ASHOK BINDRA
ELECTRONIC ENGINEERING TIMES, 1994, n 827, PG16
PUBLICATION DATE: 941212
JOURNAL CODE: EET   LANGUAGE: English
RECORD TYPE: Fulltext
SECTION HEADING: news
WORD COUNT: 567

...   Access.bus protocol that ABIG's subcommittee has drafted. That
version is designed for the   **parallel** port, but Momentum said it will
convert it to the **serial**   Access. **bus** port when the market is ready.
The product is being targeted at wireless networking, according...

 33/3,K/27   (Item 3 from file: 647)

00631025   CMP ACCESSION NUMBER: EET19890206S3811
Distributing Transputers yields bus-less parallel network
Randy Cochran, President, Paracom Inc., West Chicago, Ill.
ELECTRONIC ENGINEERING TIMES, 1989, n 524, 66
PUBLICATION DATE: 890206
JOURNAL CODE: EET   LANGUAGE: English
RECORD TYPE: Fulltext
SECTION HEADING: 524PG66
WORD COUNT: 1359

...   such as PC AT bus, PS/2, Macintosh II, Q-bus and Multibus I) via
bus bridge boards that provide a **conversion** medium from **parallel** data
on the bus to the **serial**   communications links of the Transputer.

 33/3,K/28   (Item 4 from file: 647)

00560787   CMP ACCESSION NUMBER: EBN19901001S0193
Serial Controller For FIFO Mode On MicroChannel (BRIEFS)
ELECTRONIC BUYERS' NEWS, 1990, n 722, 22
PUBLICATION DATE: 901001
JOURNAL CODE: EBN   LANGUAGE: English
RECORD TYPE: Fulltext
SECTION HEADING: PRD
WORD COUNT: 97

TEXT:
    This **serial** controller, model 16C550A, operates on the MicroChannel microcomputer I/O **bus** , handling **serial** -to- **parallel** **conversion** of data received from an external device and **converting** data from the bus from **parallel** to **serial** format.
?